

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1776	(semiconductor or chip or die) and (packag\$4 or carrier or board) and (power near5 (ring or bar or plane)) and (ground near5 (ring or bar or plane)) and (passive or inductor or capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:22
L3	652	(semiconductor or chip or die) and (packag\$5 or carrier or board) and (((power near5 (ring or bar or plane)) and (ground near5 (ring or bar or plane))) same (passive or inductor or capacitor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:13
L4	5766	(257/691 or 257/692 or 257/723 or 257/724).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:15
L5	2748	(257/691 or 257/692).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:15
L9	639	(257/691 or 257/692).ccls. and (passive or capacitor or inductor or resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:17
L10	1086	(257/723 or 257/724).ccls. and (passive or capacitor or inductor or resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:18
L11	533	(257/776 or 257/784).ccls. and (passive or capacitor or inductor or resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:19
L12	2099	(257/528 or 257/531 or 257/532). ccls. and (passive or capacitor or inductor or resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:19
L13	132	(semiconductor or chip or die) and (packag\$4 or carrier or board) and (power near5 (ring or bar or plane)) and ((ground near5 (ring or bar or plane or contact)) with (bond\$5 near4 (area or region or pad or chip or die))) and (passive or inductor or capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 06:16

L14	262	(semiconductor or chip or die) and (PWB or carrier or board) and (power near5 (ring or bar or plane)) and ((ground near5 (ring or bar or plane or contact)) with (bond\$5 near4 (area or region or pad or chip or die)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 06:16
S1	1627	(semiconductor or chip or die) and (packag\$3 or carrier or board) and (power near5 (ring or bar or plane)) and (ground near5 (ring or bar or plane)) and (passive or inductor or capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:12
S2	598	(semiconductor or chip or die) and (packag\$3 or carrier or board) and (((power near5 (ring or bar or plane)) and (ground near5 (ring or bar or plane))) same (passive or inductor or capacitor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/04 05:13